| Search Notes | |
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| Application No. | Applicant(s) |
|-----------------|----------------------|
| 10/643,193 | BHAVNAGARWALA ET AL. |
| Examiner | Art Unit |
| Jeffrey R. West | 2857 |

| | SEAR | CHED | |
|-------|-------------------|-----------|----------|
| Class | Subclass | Date | Examiner |
| 702 | 57,58,64 65,66 | 1/23/2005 | JRW |
| 702 | 85,182 | 1/23/2005 | JRW |
| 702 | 117 | 1/13/2005 | JRW |
| 702 | 189 | 1/23/2005 | JRW |
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| TNI | INTERFERENCE SEARCHED | | |
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| SEARCH NOT (INCLUDING SEARCH S | |) |
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| | DATE | EXMR |
| Inventor Search Online Search IEEE Xptore | 1/13/2005 | JRW |
| (threshold or thresholds) with (voltage or voltages) with (mismatch or mismatches or mismatching or mismatched) | 1/13/2005 | JRW |
| (((vary or varies or varied or varying) with (gate) near (voltage or voltages)) with (transistor or transistors or fet or fet | 1/13/2005 | JRW |
| s or mosfet or mosfets or cmosfet or cmosfets)) same (subthreshold or subthresholds or sub-threshold or sub- thresholds or (b | 1/13/2005 | JRW |
| elow adj2 (threshold or thresholds))) | 1/13/2005 | JRW |
| threshold with voltage with (mismatch or mismatches or mismatching or mismatched or mis-match or mis- matches or mis-matching | 1/19/2005 | JRW |
| or mis-matched or match or matches or matching or matched) with (transistor or transistors) with (pair or pairs or paired) | 1/19/2005 | JRW |
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